

# Fig. 2

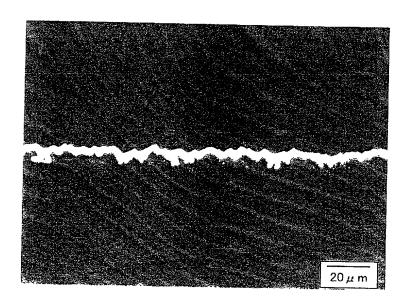
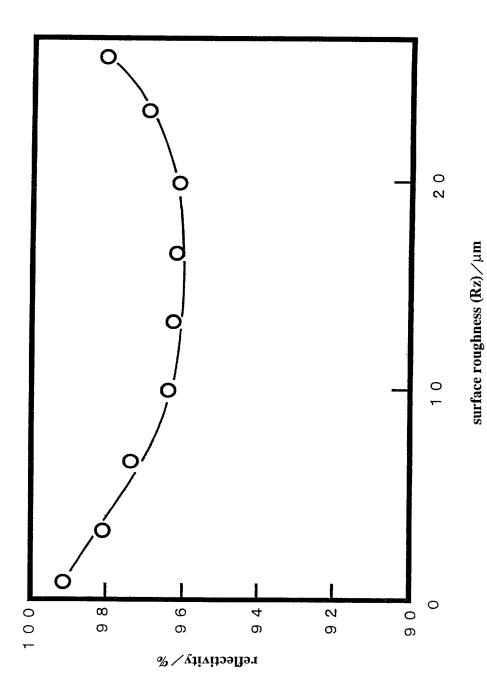
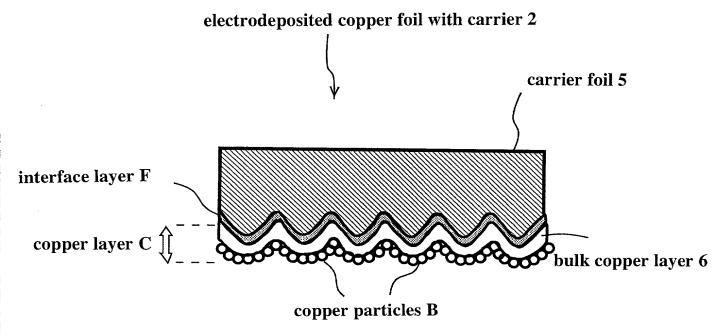


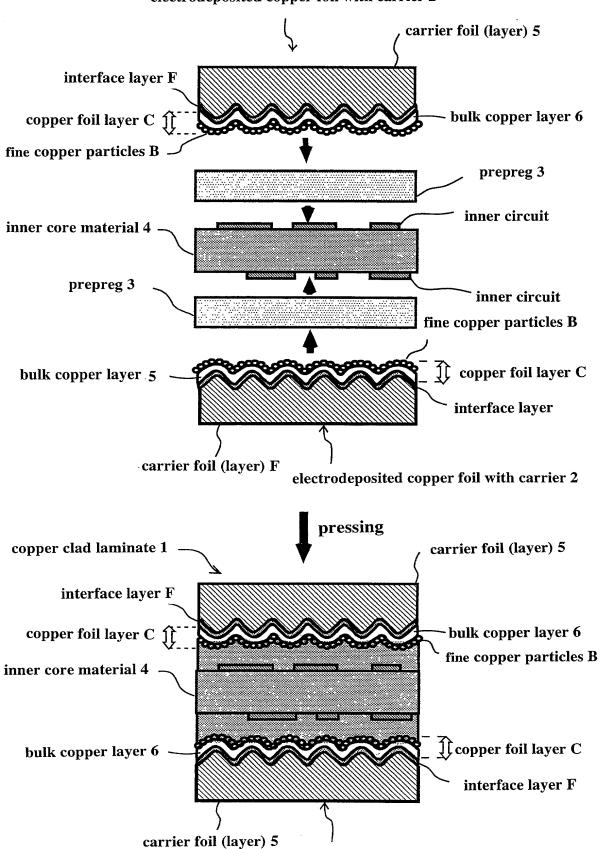
Fig. 3





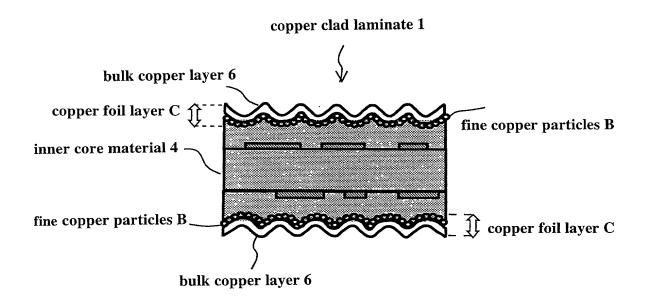
#### (a) Manufacturing of copper clad laminate

#### electrodeposited copper foil with carrier 2

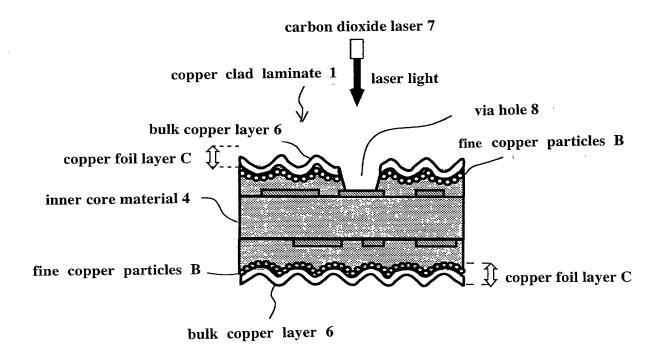


electrodeposited copper foil with carrier 2

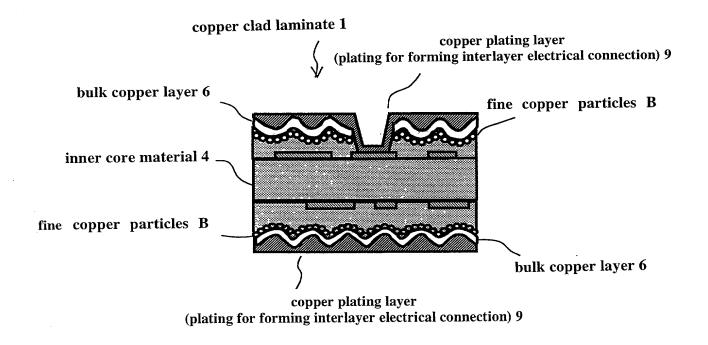
#### (b) Peeling of Carrier Foil



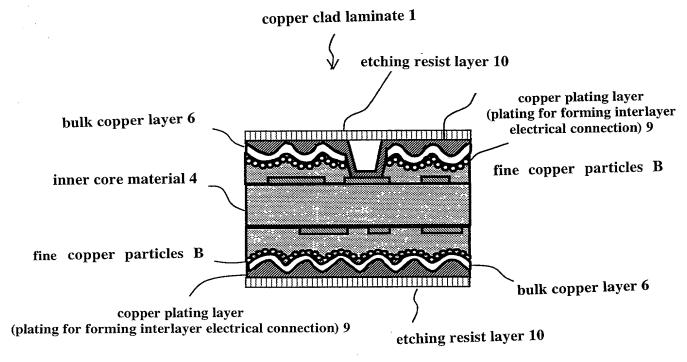
#### (c) Laser Treatment for Forming Holes



### (d) Copper Plating for Forming Interlayer Electrical Connection



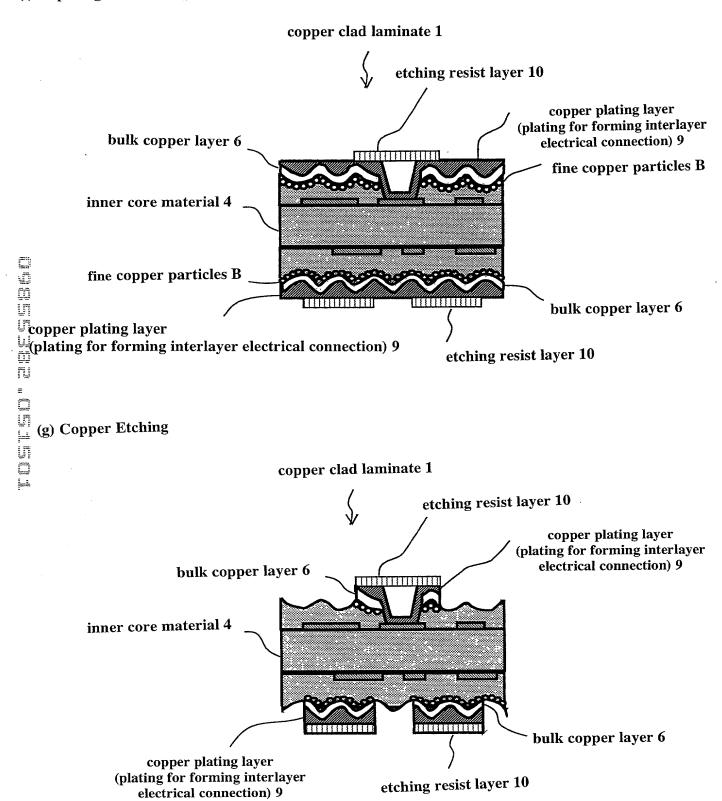
### (e) Forming Etching Resist Layers



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### Fig. 8

### (f) Exposing and Developing the Etching Resist Layers



# (h) Peering of Etching Resist Layers

